

Model Name:GA-G41MT-S2P

Revision 1.31

SHEET

TITLE

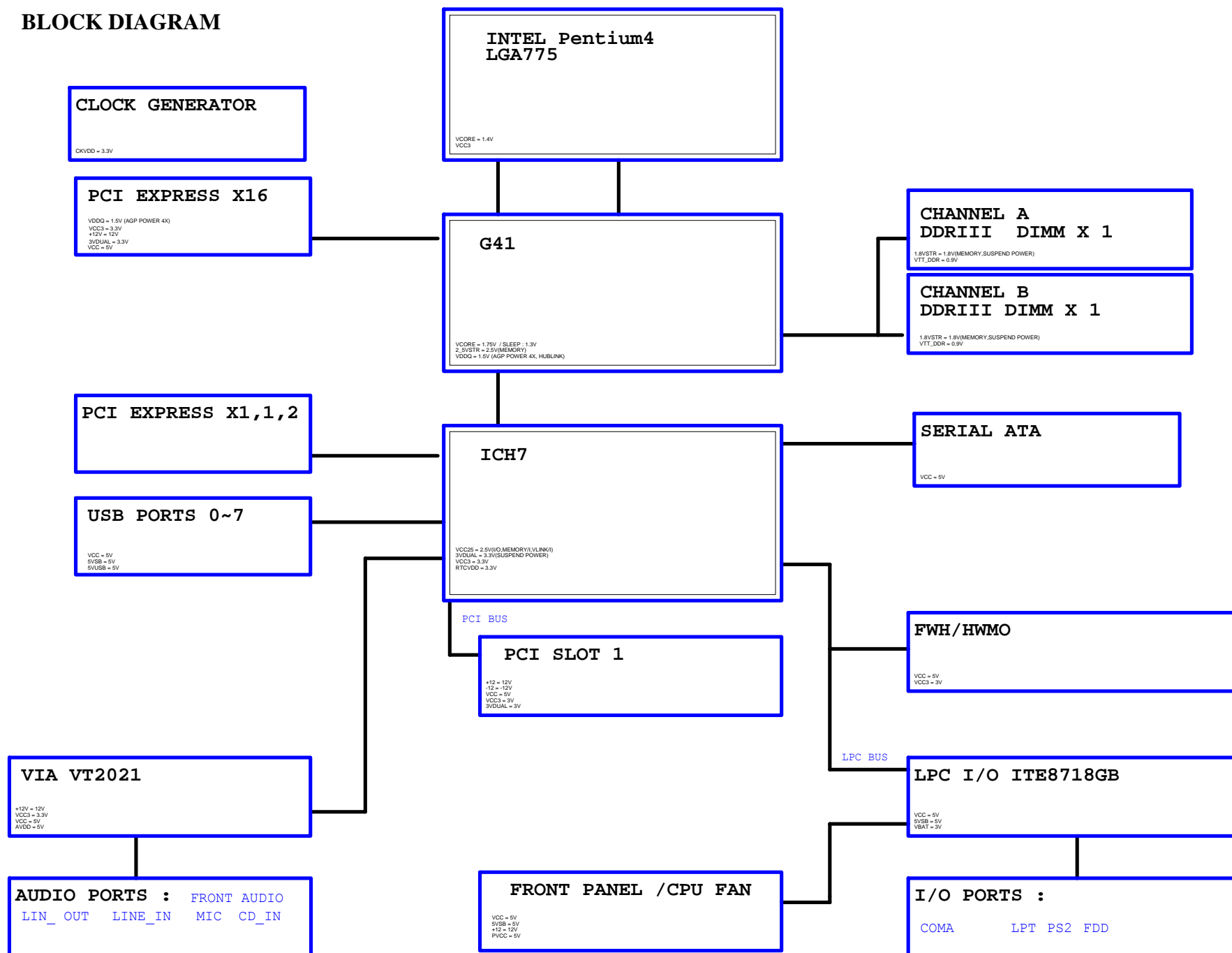
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B,D
06	P4_LGA775_C
07	P4_LGA775_E,F,G,H
08	G41_HOST
09	G41_DDRII
10	G41_PCI E, DMI
11	G41_VGA
12	G41_GND
13	G41_PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,PCIE*1,2
23	IDE/FLOPPY/F_USB/R_USB
24	ITE 8718 GB
25	COM_LPT
26	CI,HWM,KB/MS,DUALBIOS
27	VIA VT2021

SHEET

TITLE

28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM_ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	ARTHEROS AR8151

BLOCK DIAGRAM



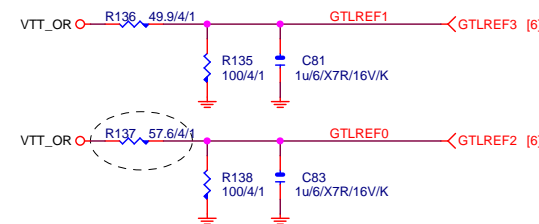
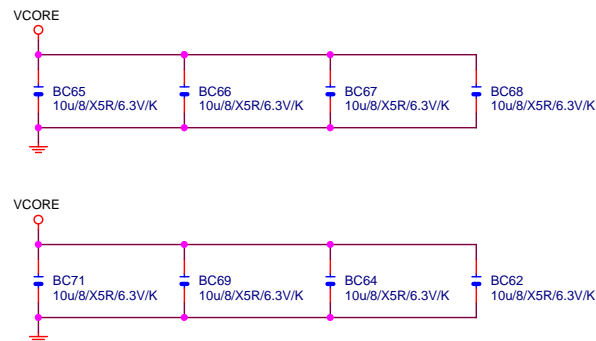
<http://rusefix.com>

Gigabyte Technology			
BOM & PCB MODIFY HISTORY			
File	Document Number	Rev	1.3
Size	GA-G41MT-S2P		
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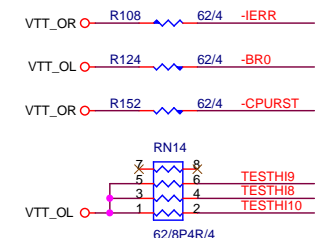
Version: 1.31

2010/12/23

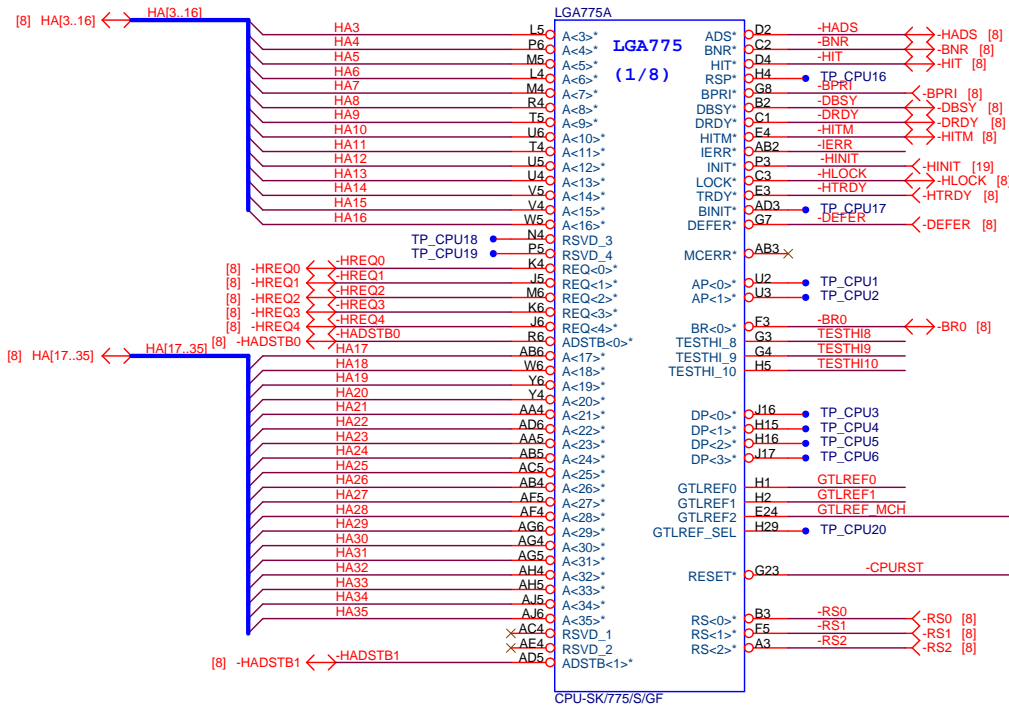
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中間値 0.9V



LGA775-39-2

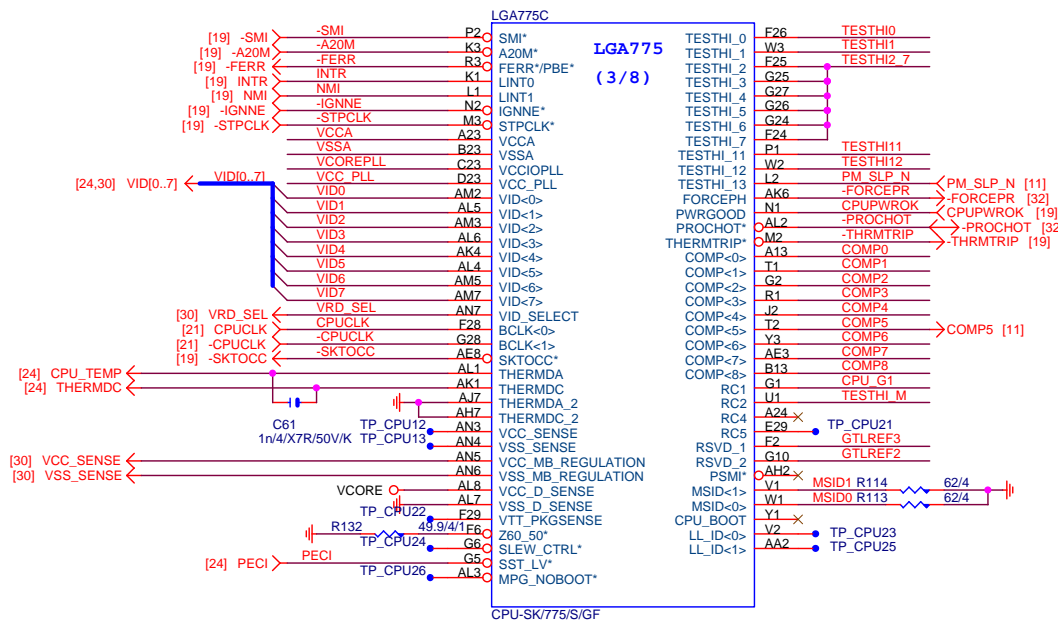
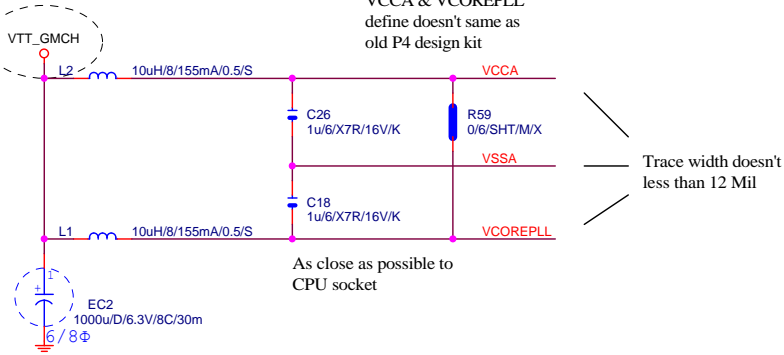


Gigabyte Technology

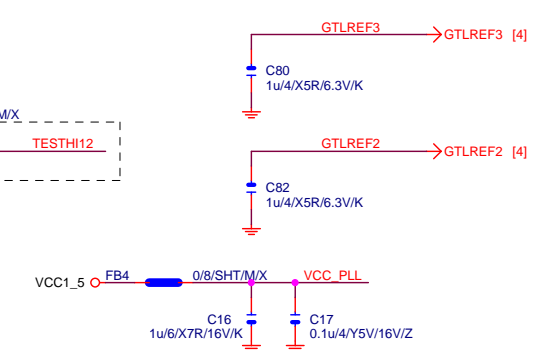
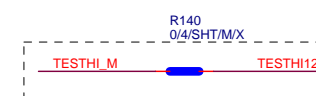
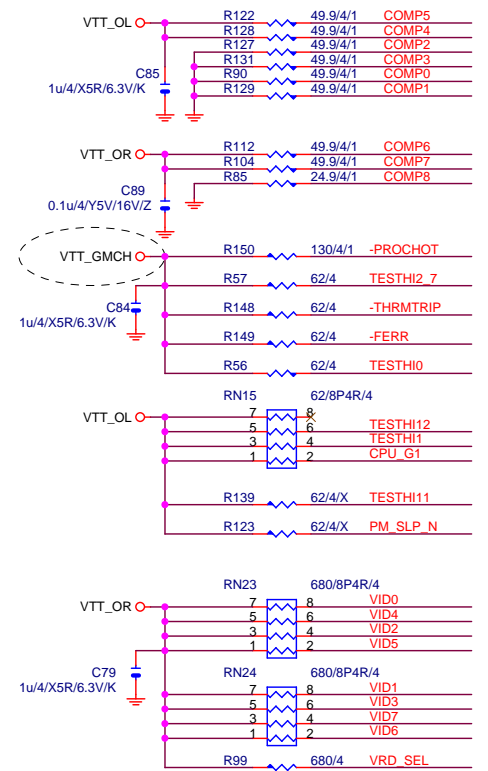
Title			P4_LGA775-A
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Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



Place outside of CPU socket

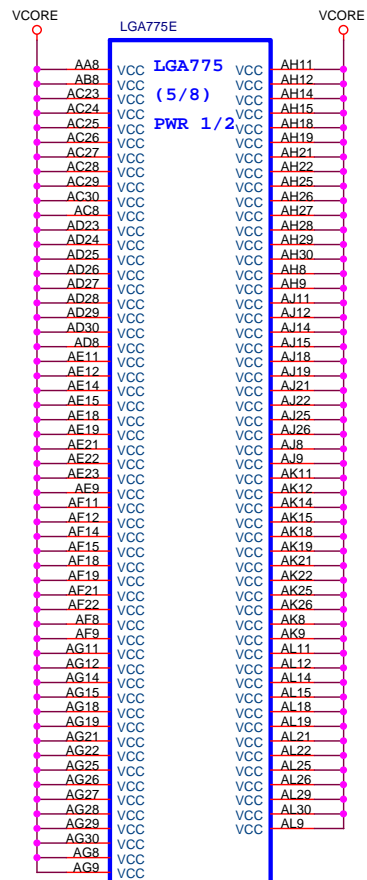


Gigabyte Technology

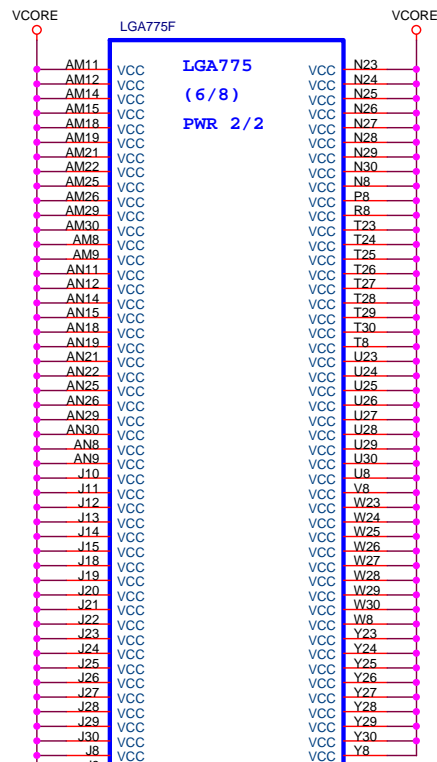
Title		
P4_LGA775-C		
Size	Document Number	Rev
B	GA-G41MT-S2P	1.3
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PECI:Platform Environment Control Interface

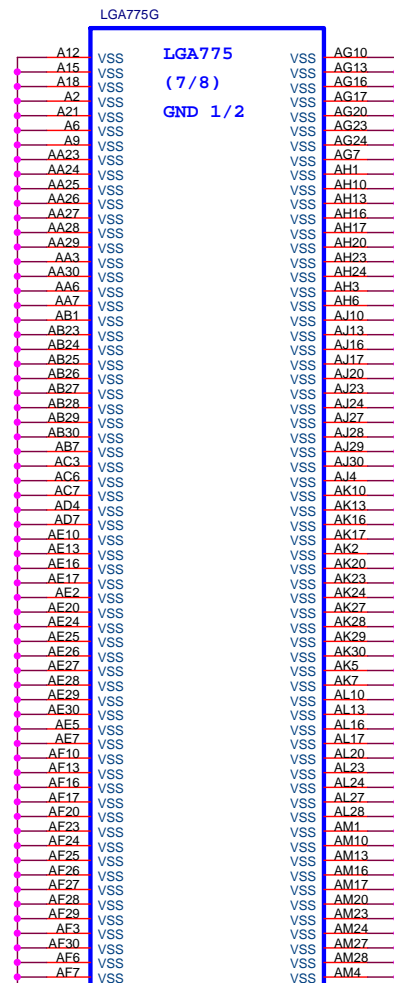
<http://rusefix.com>



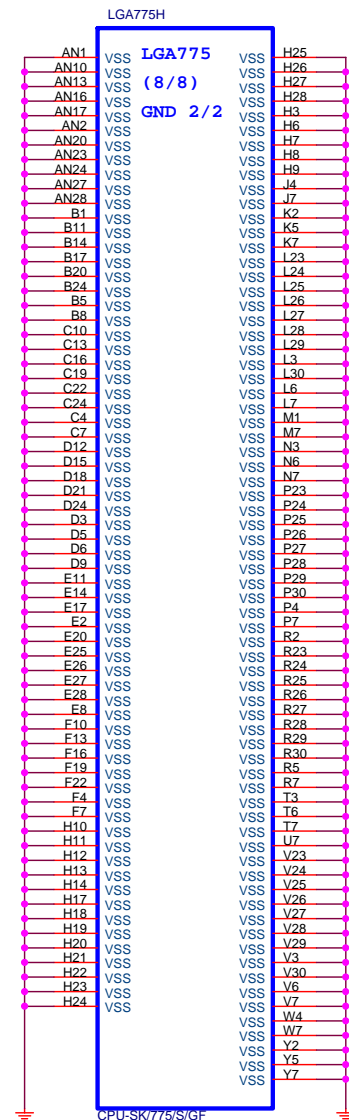
CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF

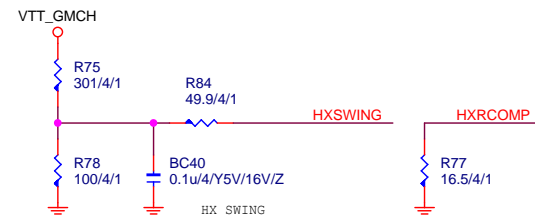


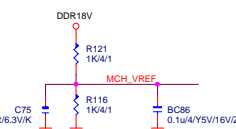
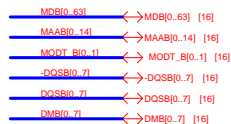
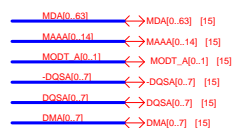
CPU-SK/775/S/GF

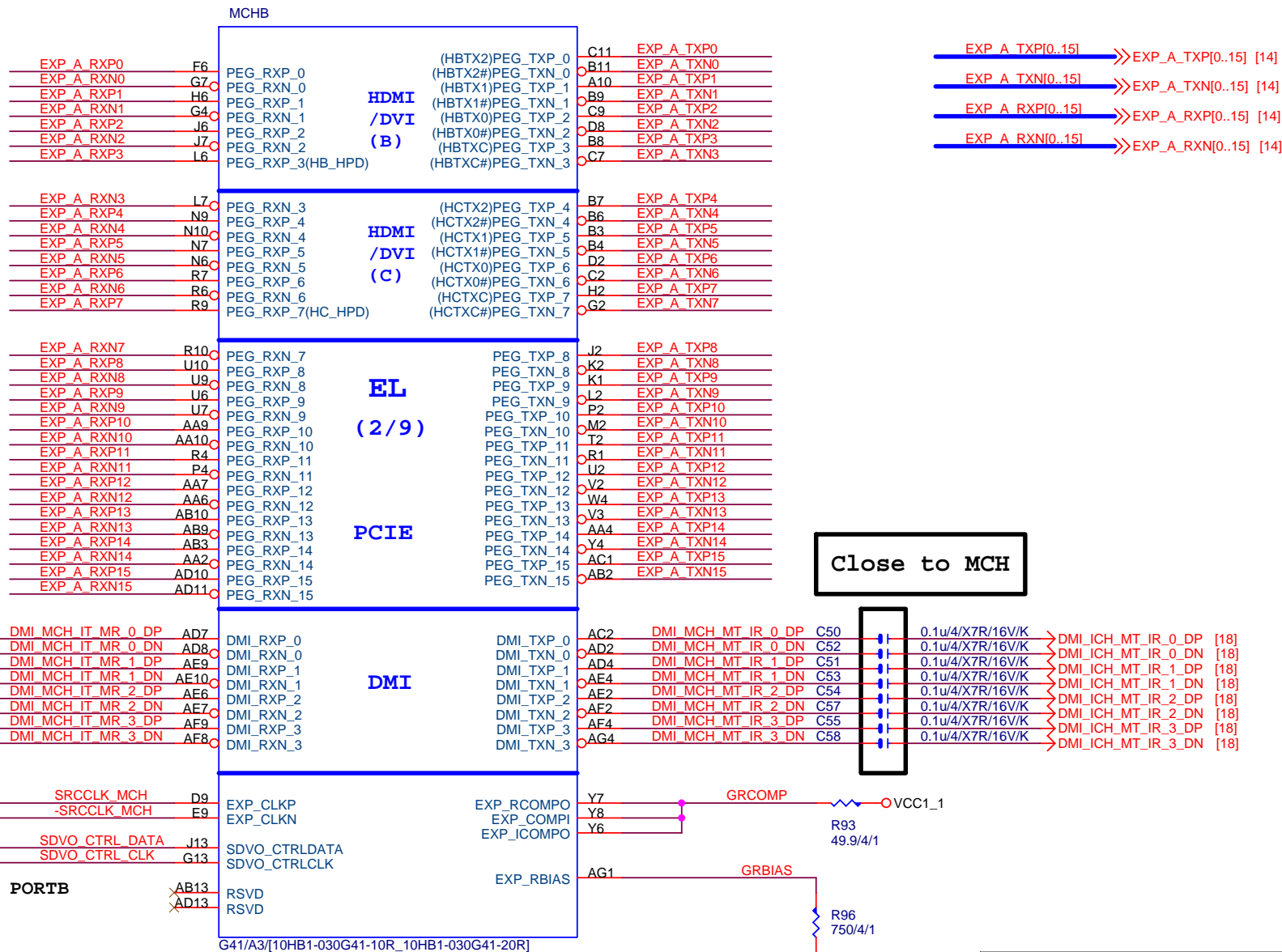
Gigabyte Technology

Title			P4_LGA775-E,F,G,H	
Size B	Document Number		GA-G41MT-S2P	
			Rev 1.3	
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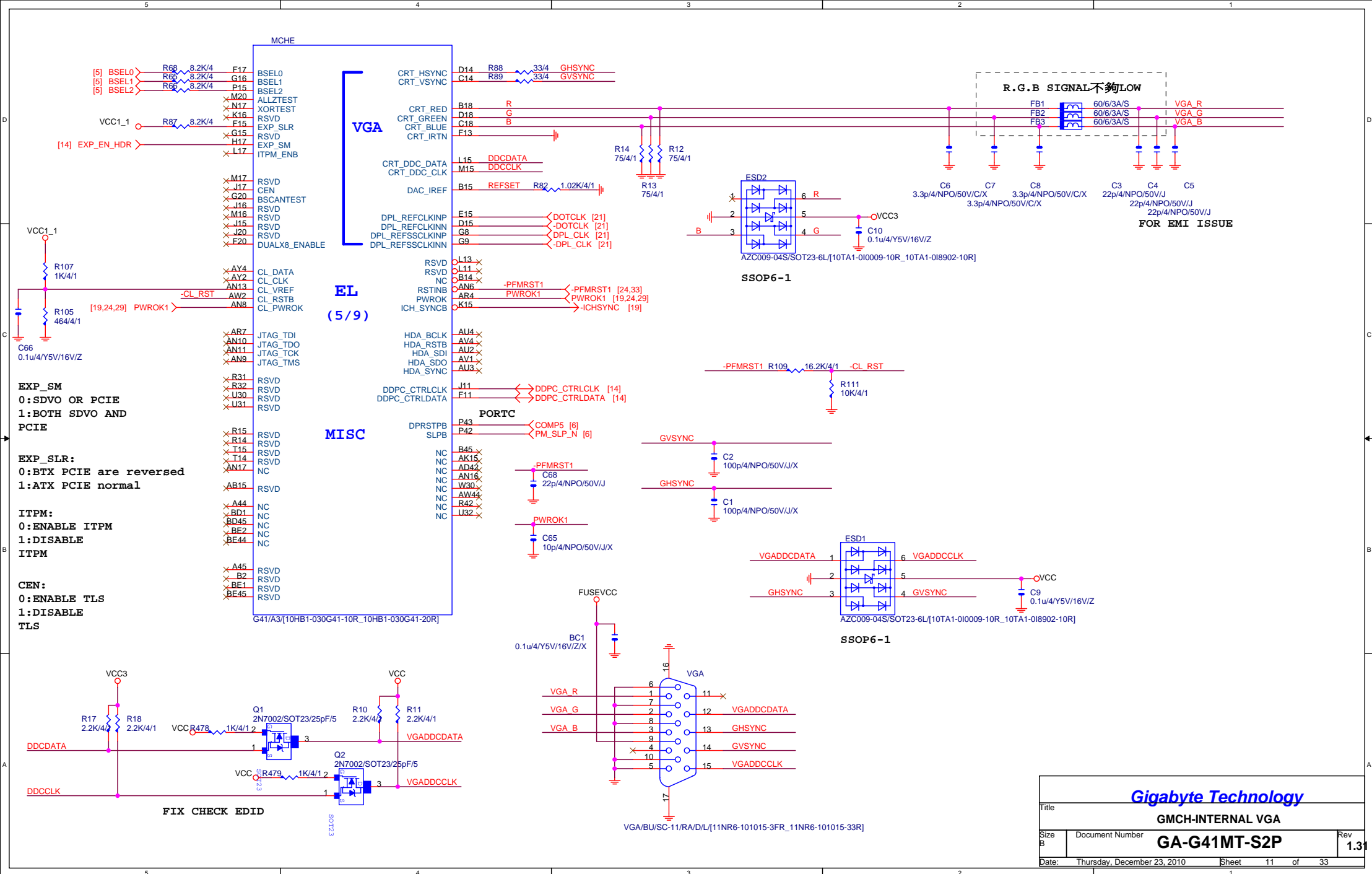
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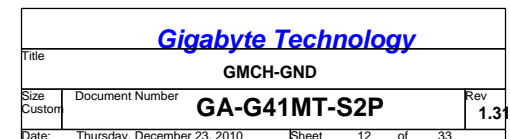
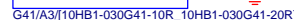
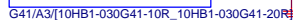


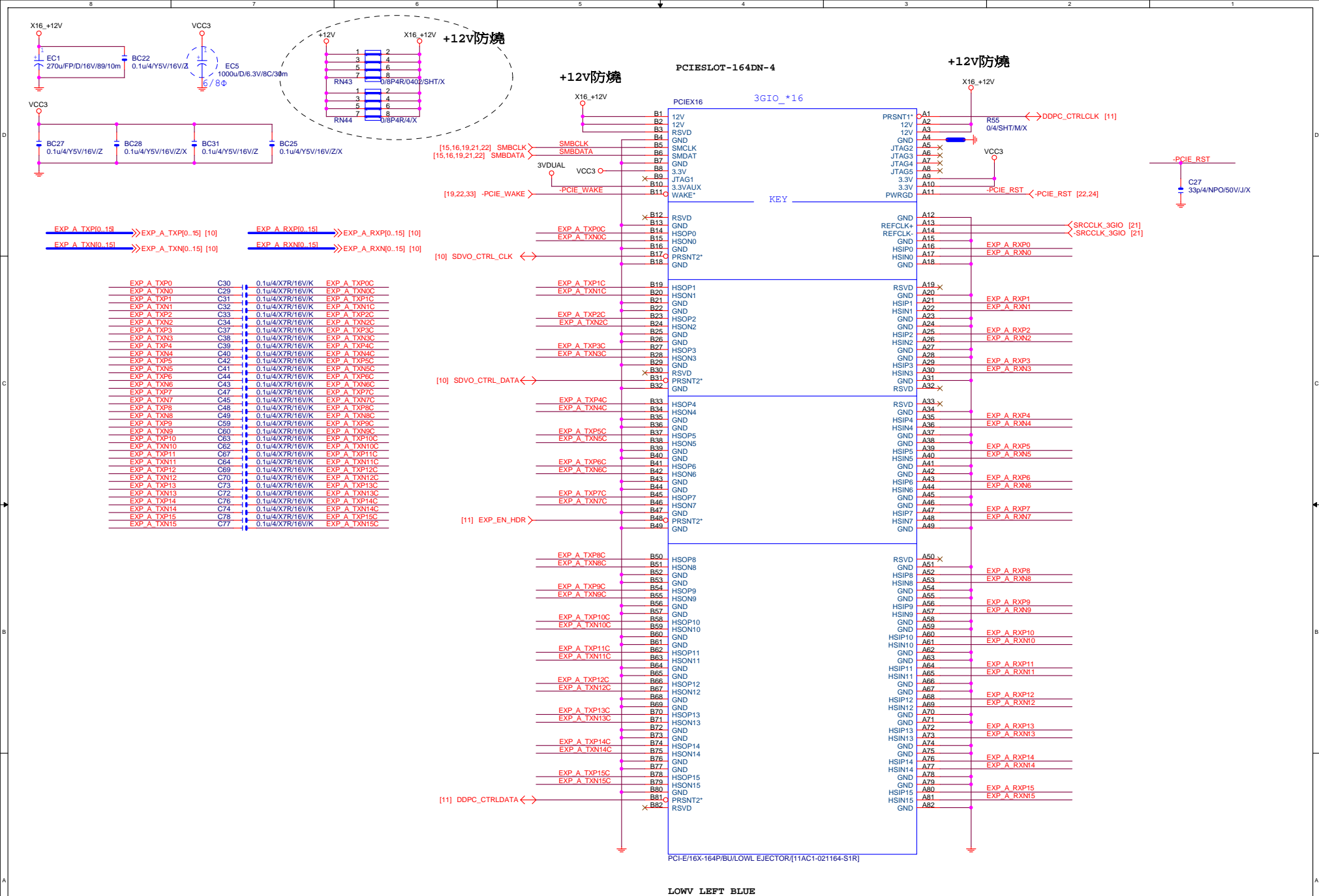




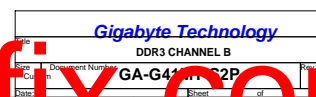
Gigabyte Technology			
Title			
GMCH-PCI E & DMI			
Size	Document Number	Rev	
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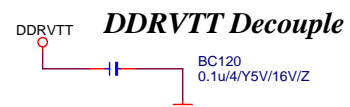
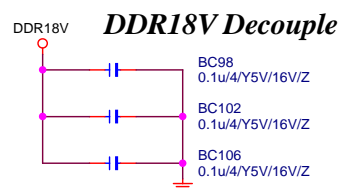
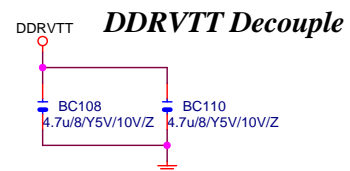
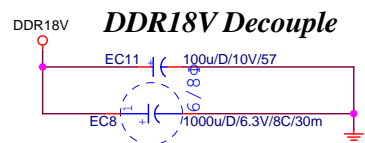




<http://rusefix.com>

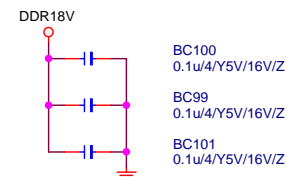


DDR TERMINATION CHANNEL A

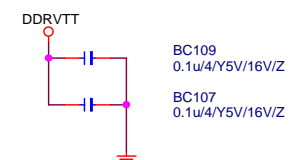


DDR TERMINATION CHANNEL B

DDR18V Decouple



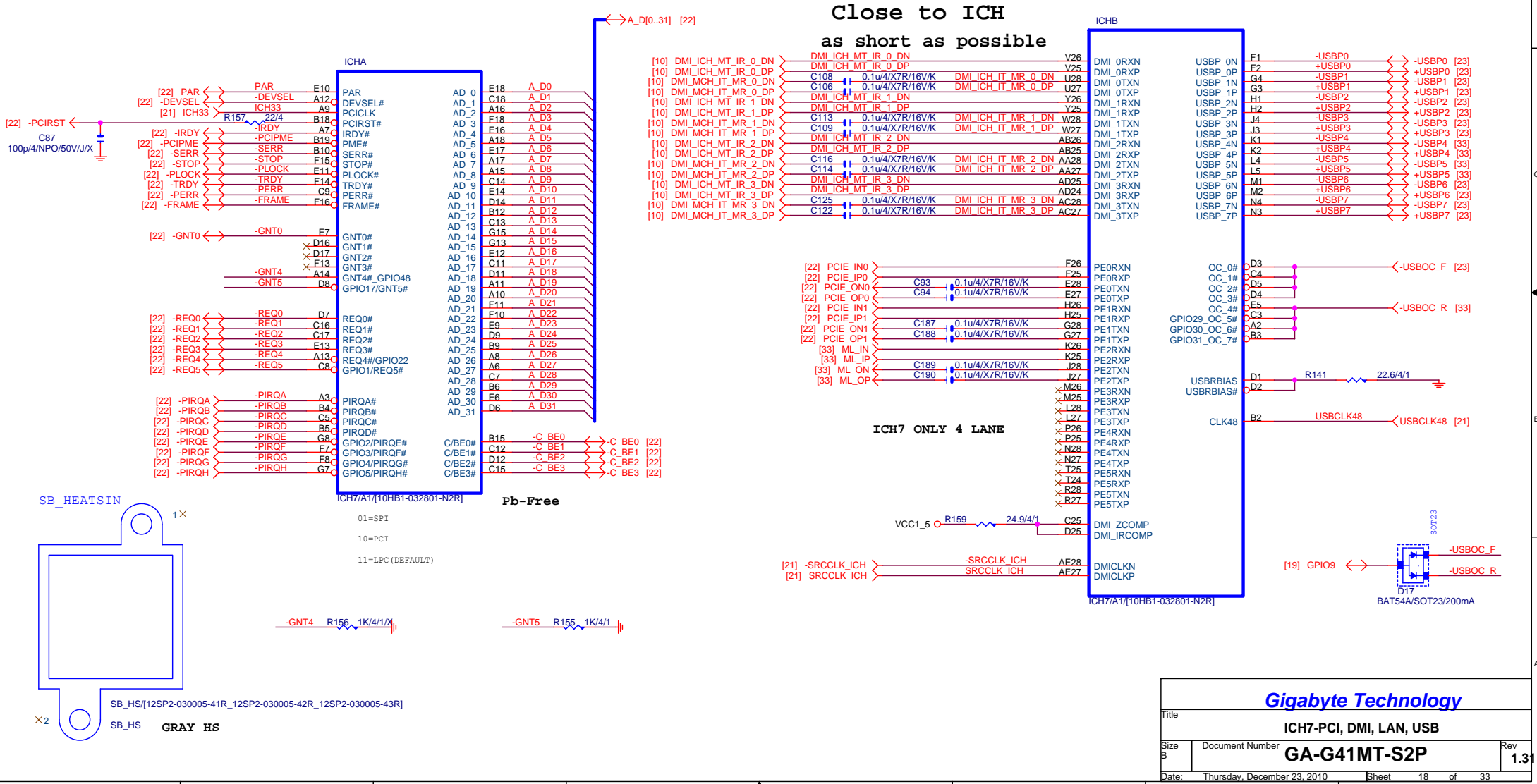
DDRVTT Decouple

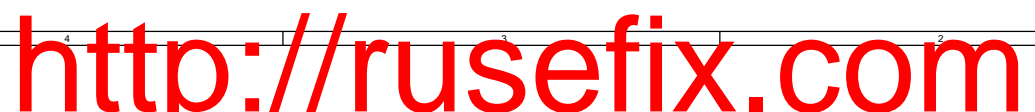


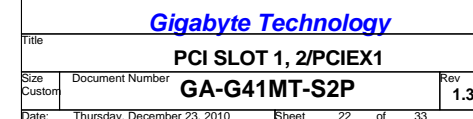
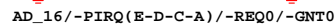
Gigabyte Technology

Title			DDRII TERMINATOR
Size	Document Number	GA-G41MT-S2P	
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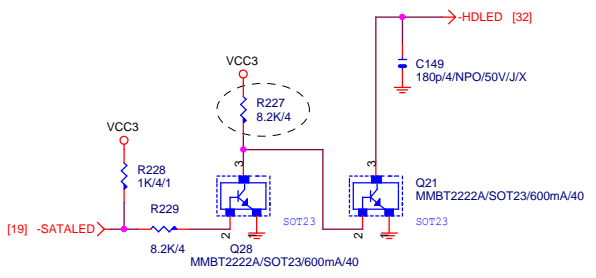
<http://rusefix.com>







IDE/SATA LED



IDE RESET

N/A

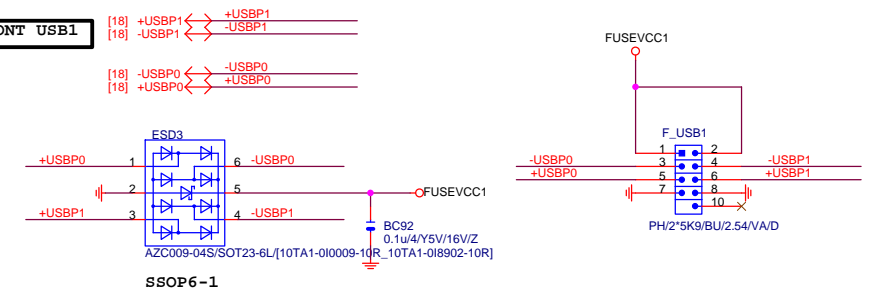
FLOPPY

N/A

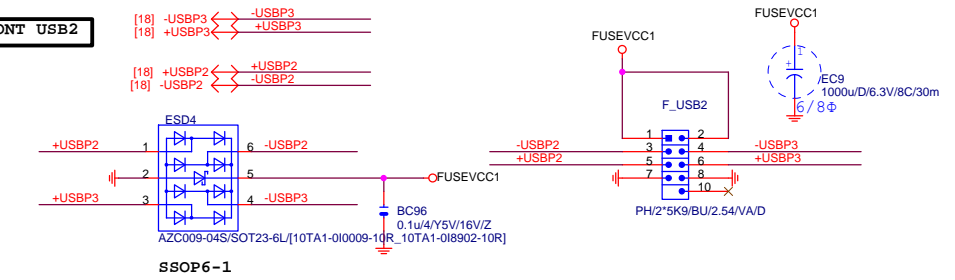
IDE

N/A

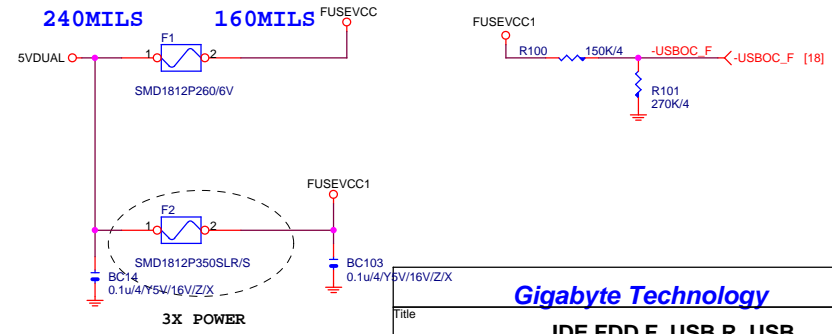
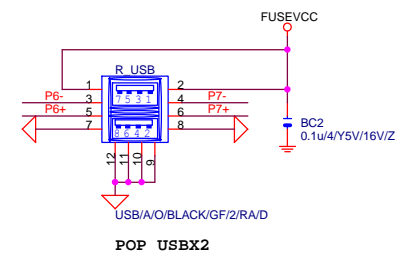
FRONT USB1



FRONT USB2



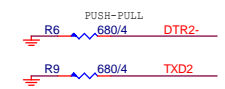
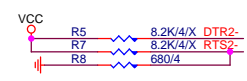
- [18] -USBP7 <-> -USBP7
- [18] +USBP7 <-> +USBP7
- [18] -USBP6 <-> -USBP6
- [18] +USBP6 <-> +USBP6



Gigabyte Technology			
Title			
IDE,FDD,F_USB,R_USB			
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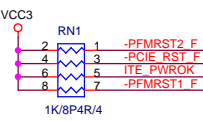
<http://rusefix.com>

RTS2- ==LOW CPU FAN 50%
==HIGH 100%
DEFAULT 50%



FOR AR8161

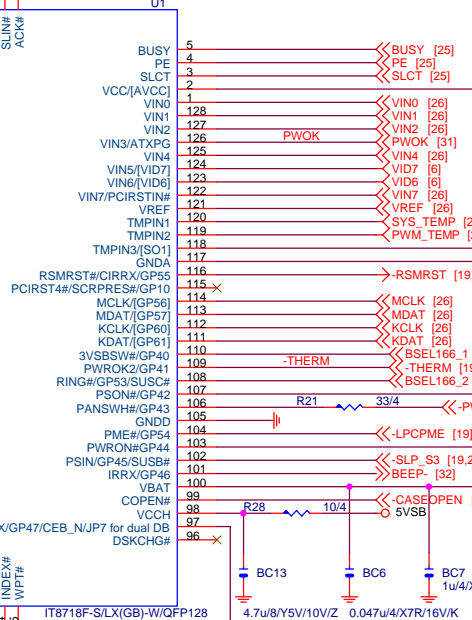
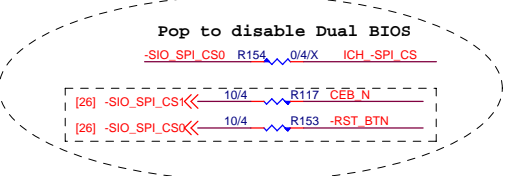
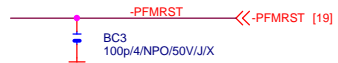
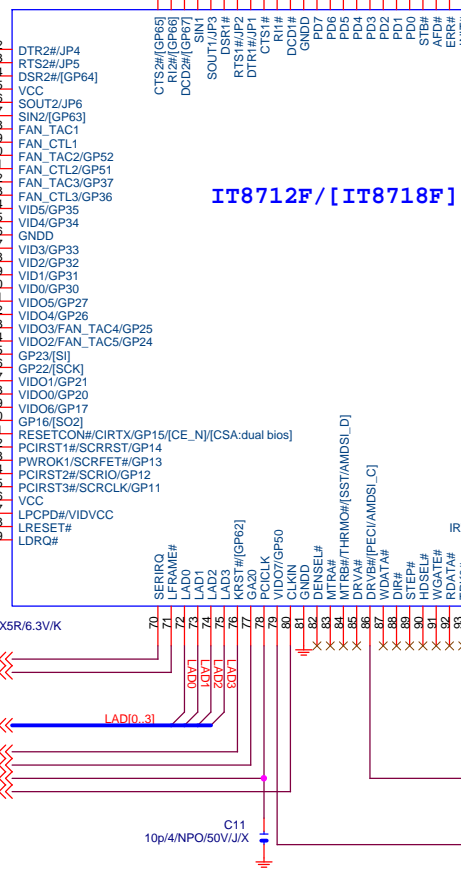
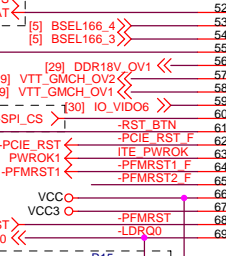
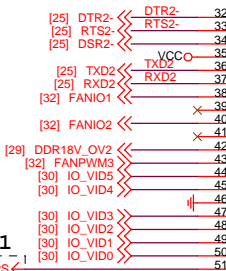
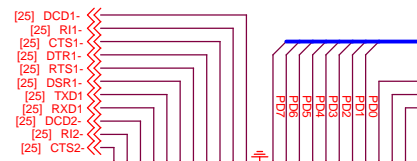
EUP [29] EUP_N



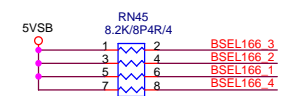
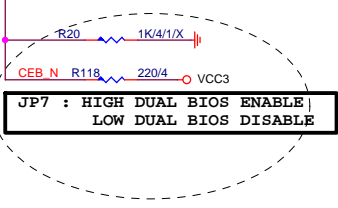
Dual BIOS:
GB logo :Pin 61 (GP15/CSA)
GB logo :Pin 59 (GP17/CSB)

Pin 59 Dual BIOS ,Power On Strapping:
H ==>Dual BIOS function Enable
L ==>Dual BIOS function Disable

1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接3.3V
LPCPD#=VIDVCC

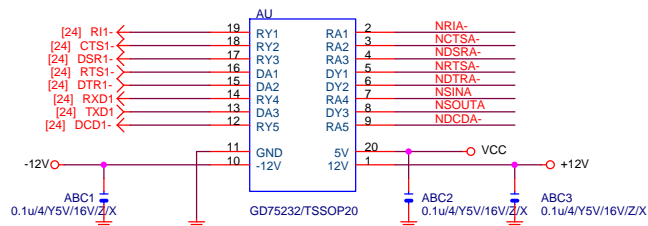


JP7 : HIGH DUAL BIOS ENABLE
LOW DUAL BIOS DISABLE



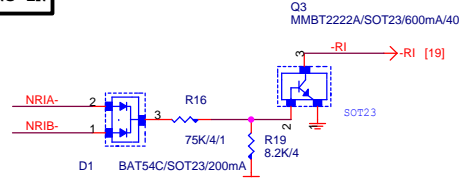
http://rusefix.com

COMA

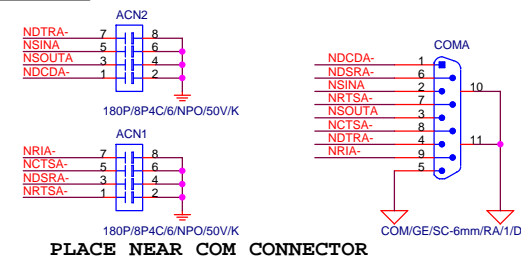


IC20TSSOP-1

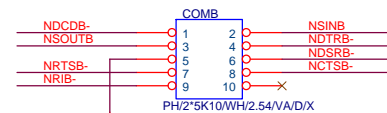
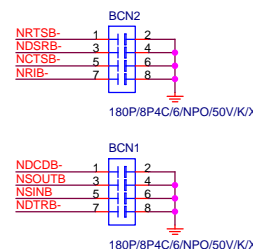
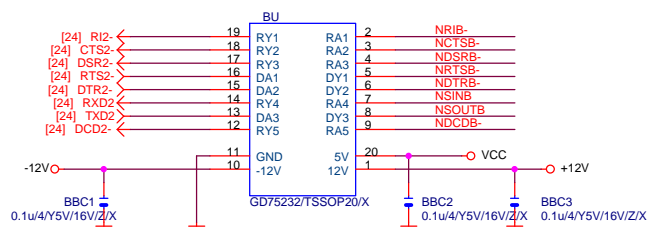
RING IN



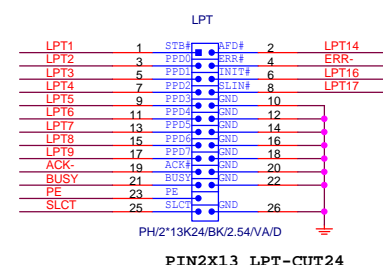
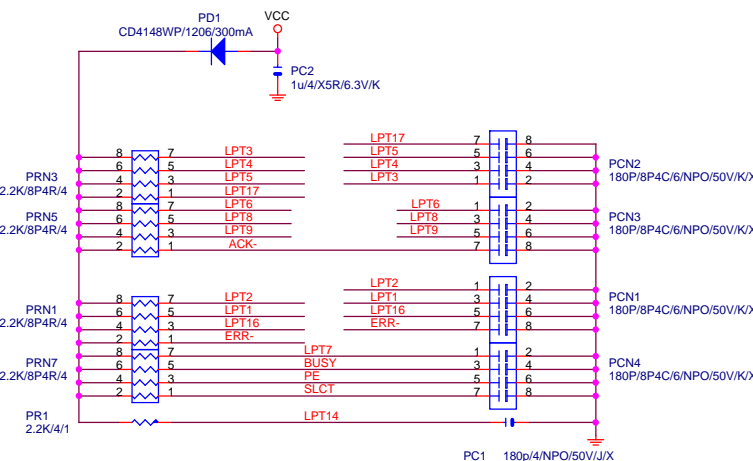
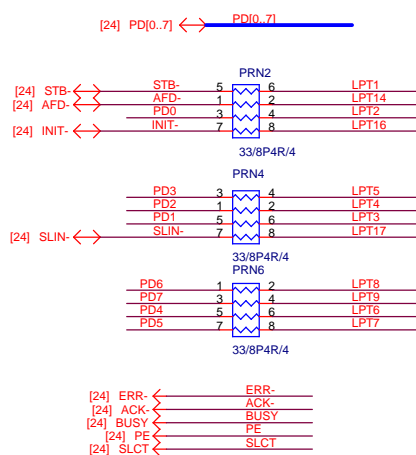
EXTERNAL COMA



PLACE NEAR COM CONNECTOR



LPT PORT



PIN2X13_LPT-CUT24

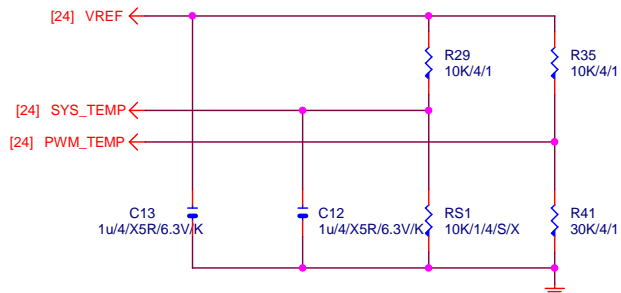
Gigabyte Technology

COM & LPT PORT

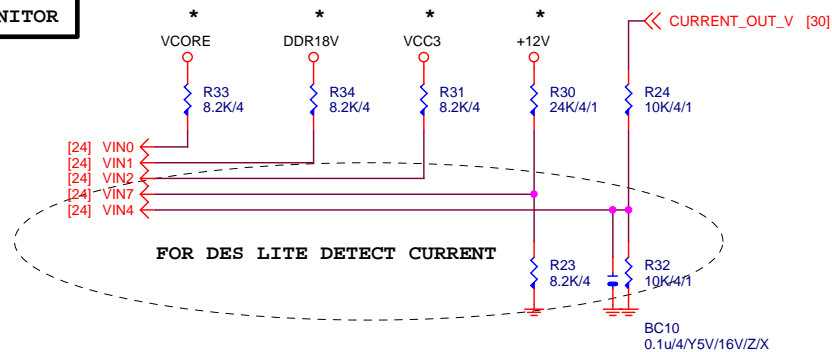
Title	COM & LPT PORT	Rev	1.31
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http://rusefix.com

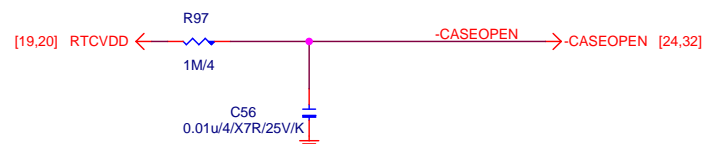
TEMP H/W MONITOR



VOLTAGE-- H/W MONITOR

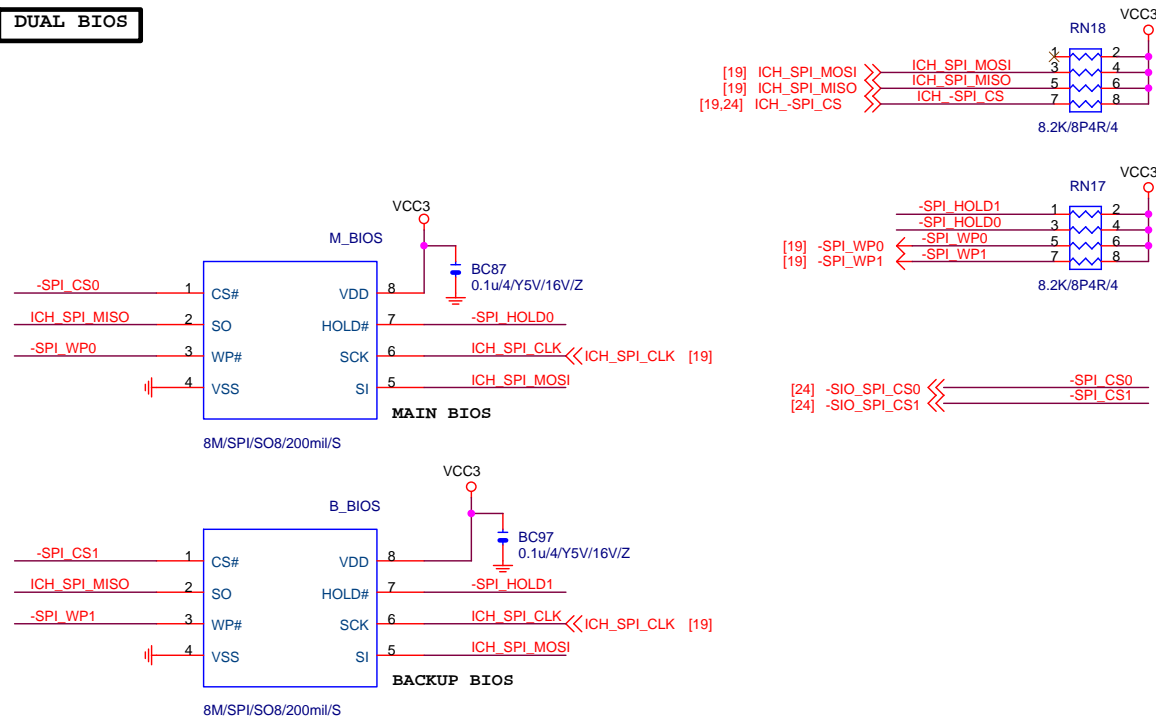


CASE OPEN

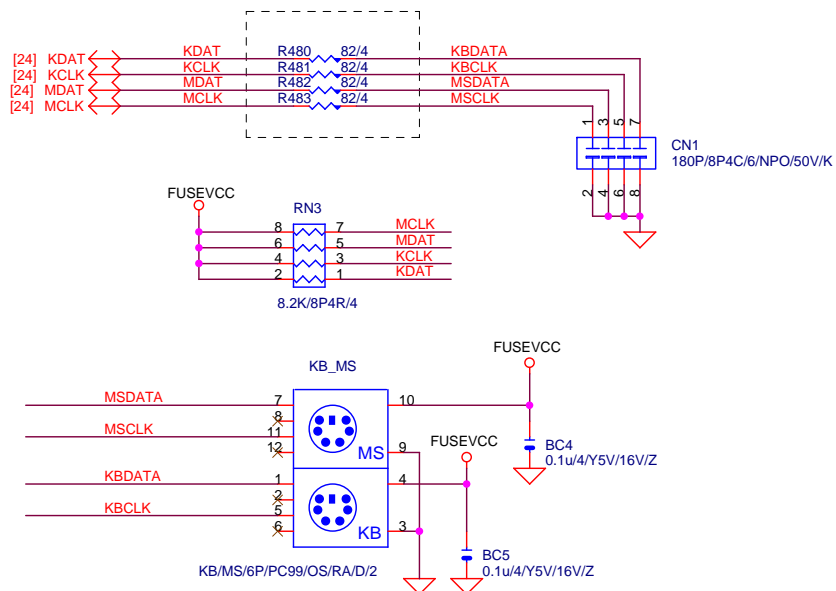


Case Open Circuits

DUAL BIOS



KB/MS

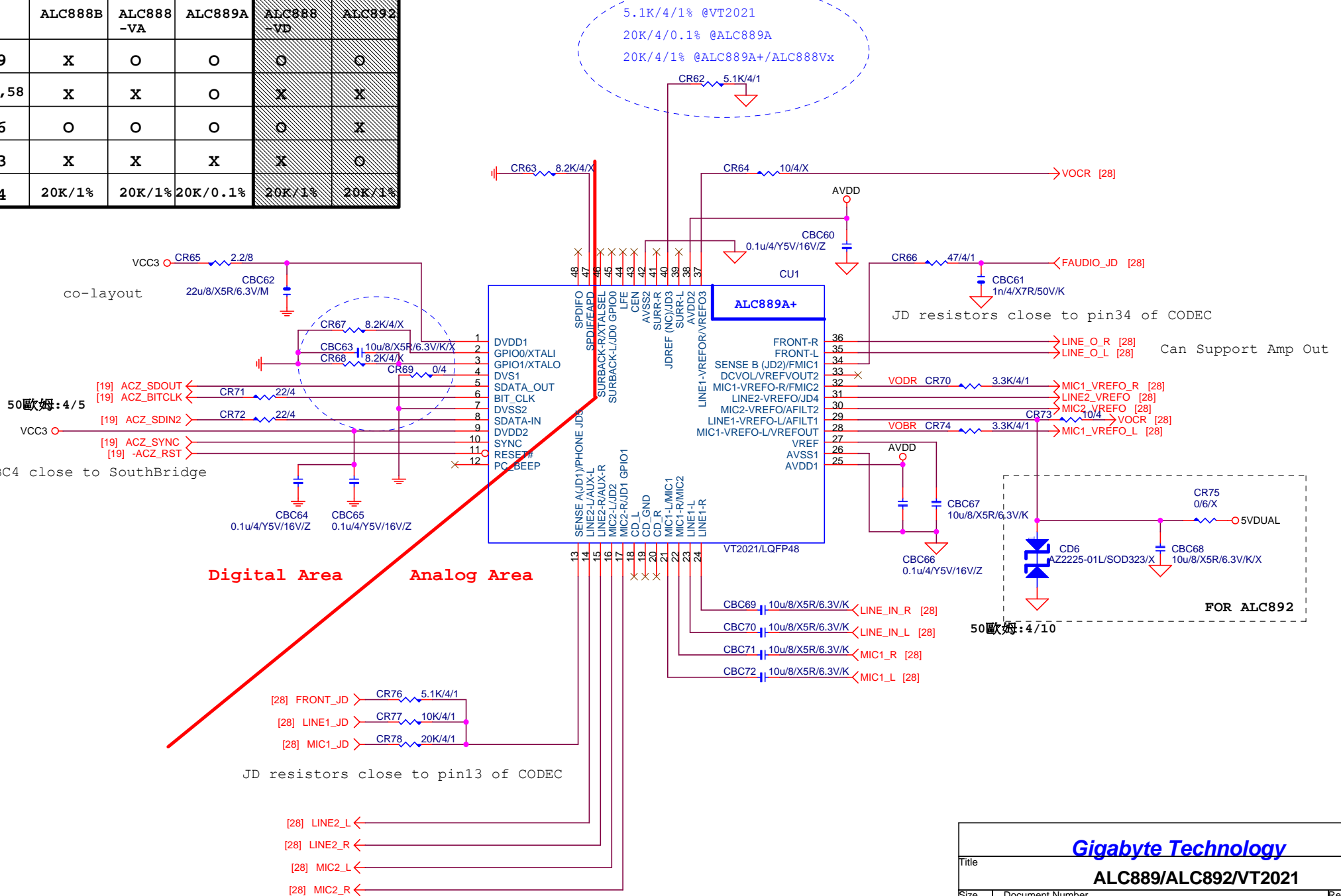


Gigabyte Technology

Title			
HW-MONITOR/CI/KB/MS/BIOS			
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<http://rusefix.com>

	ALC888B	ALC888 -VA	ALC889A	ALC888 -VD	ALC892
CR59	X	O	O	O	O
CR53, 58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%

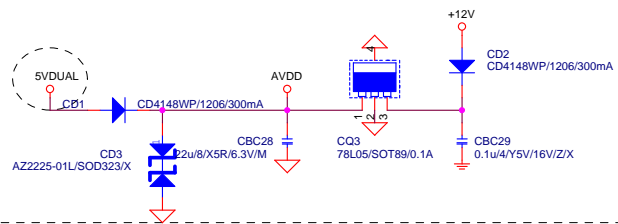


Can Support Amp Out

Gigabyte Technology

Title			ALC889/ALC892/VT2021		
Size	Document Number	GA-G41MT-S2P			Rev
Custom					1.31
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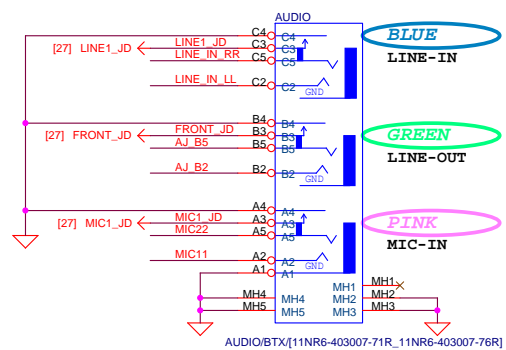
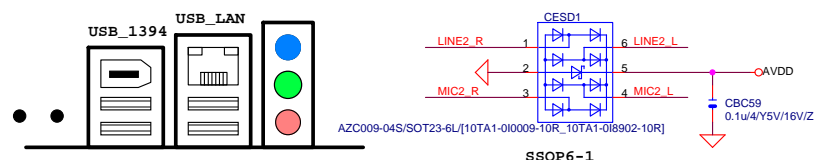
CODEC POWER/EMI PAD



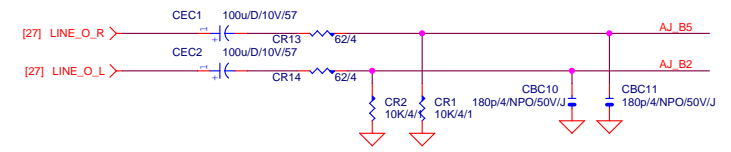
CD IN N/A

SPDIF IN N/A

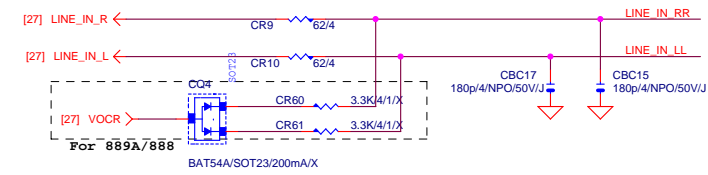
AZALIA JACK



LINE-OUT

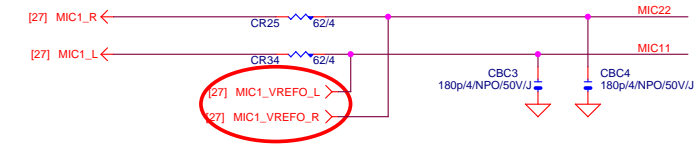


LINE-IN

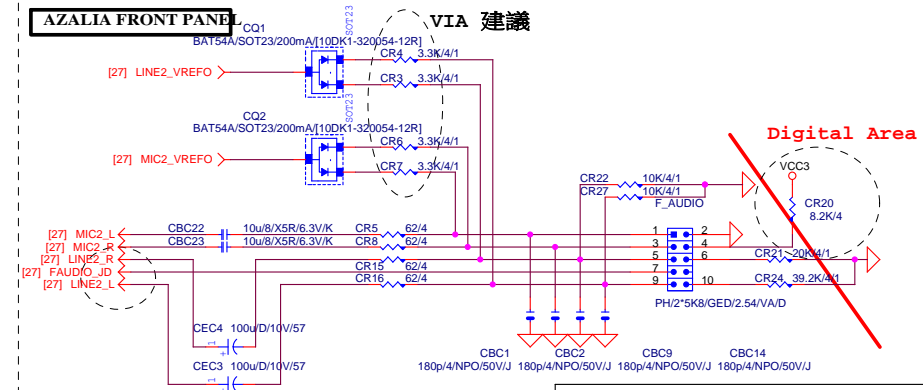


VIA 不SUPPORT

MIC-IN

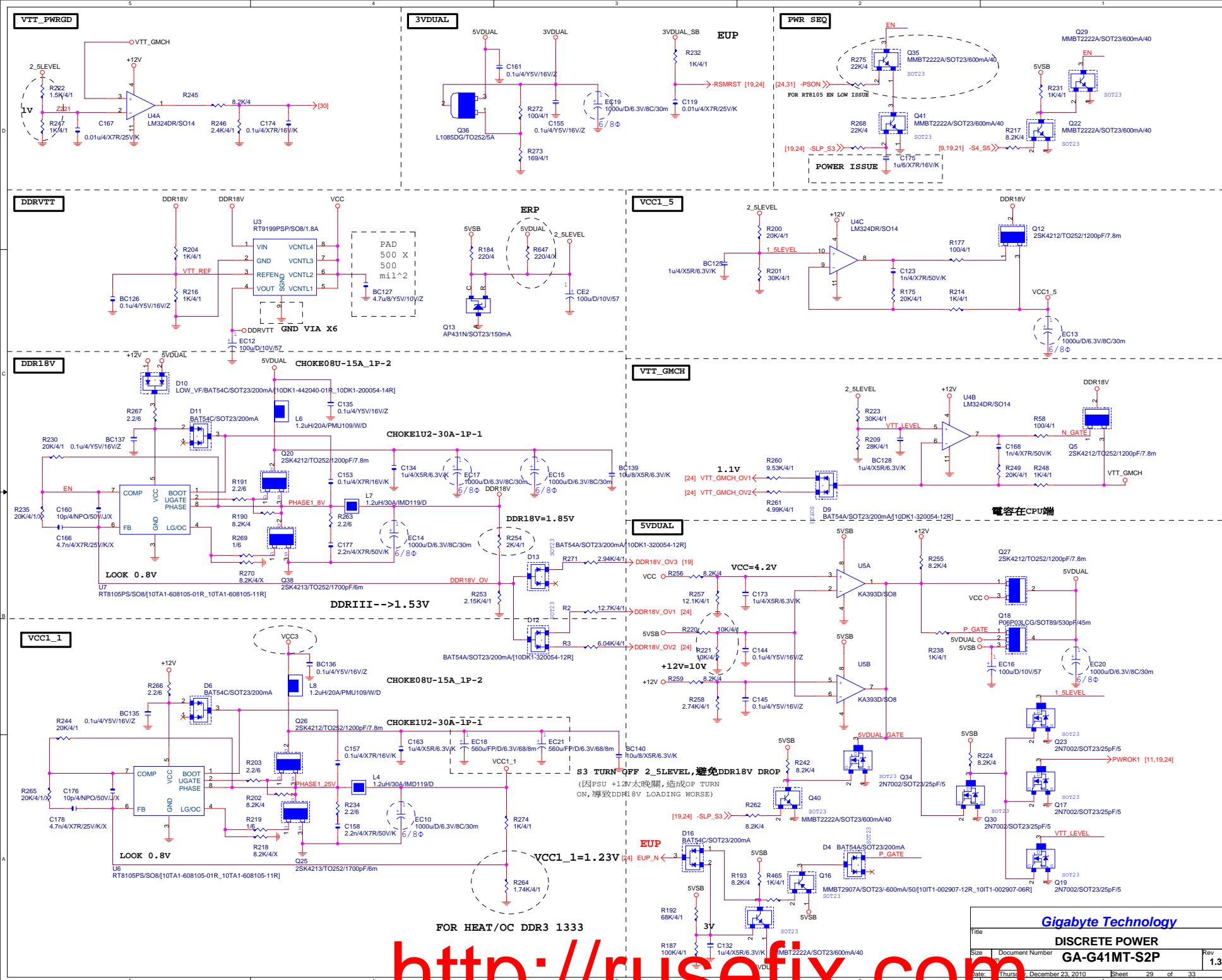


AZALIA FRONT PANE

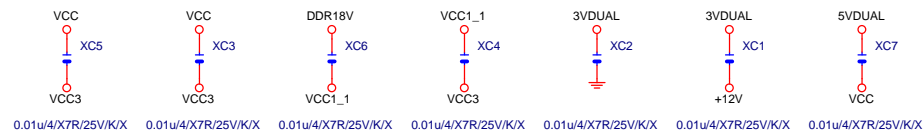
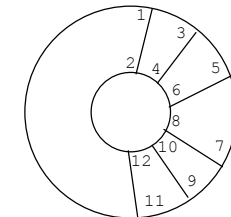
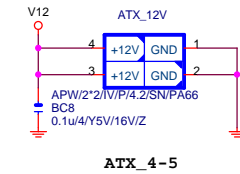
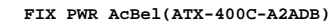


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Title			
AUDIO JACK			
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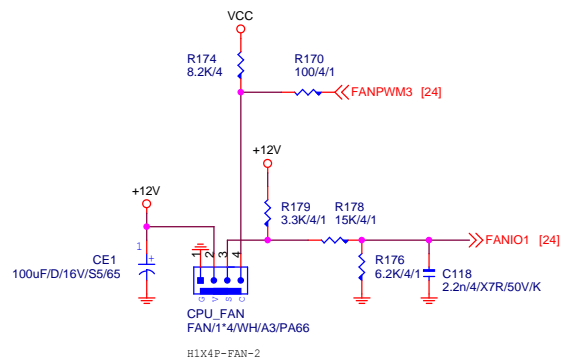


ATXPWR_24-4

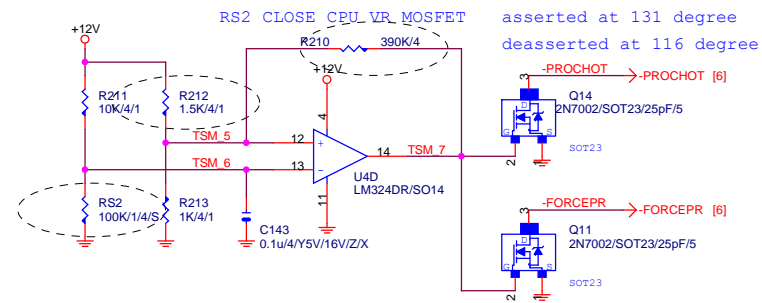


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ATX POWER CONNECTOR			
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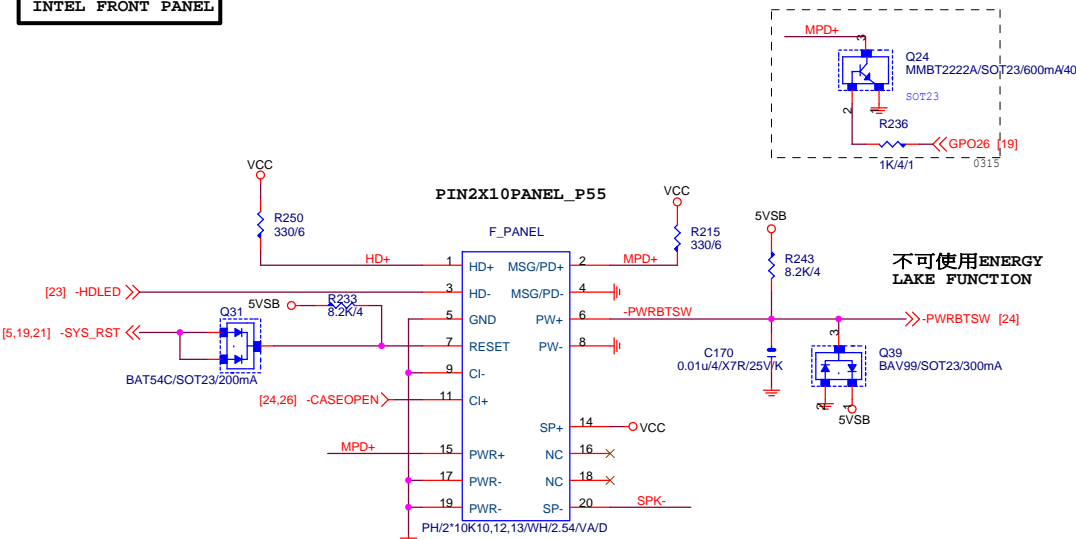
CPU SMART FAN SMART FAN



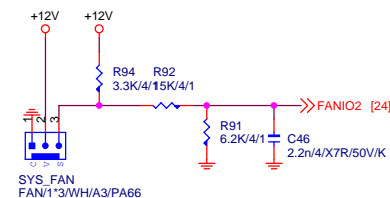
PROCESSOR HOT



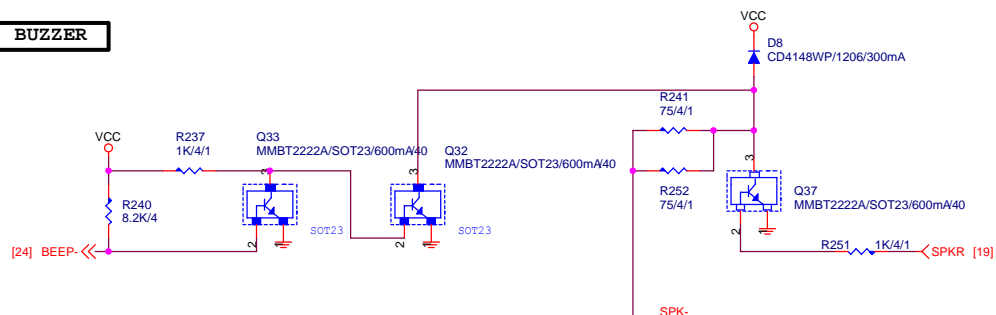
INTEL FRONT PANEL



SYS_FAN



BUZZER



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FRONT PANEL			
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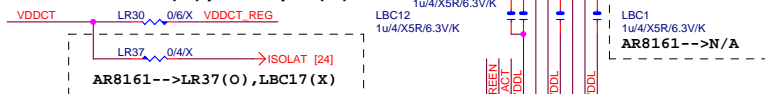
<http://rusefix.com>

PCIE-1G LAN

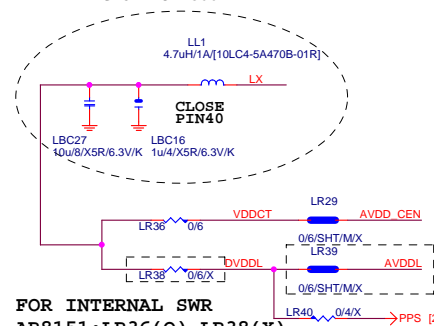
AR8131/AR8132 SWR:
LL1, LBC27, LBC16(O), LR30(X)

AR8152 LDO OUTPUT (PIN4): LL1(X)

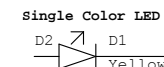
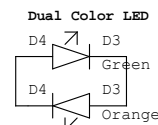
1. AL1A: LR30(O), LBC27, 26(O)
2. BL1A: LR30(O), LBC27, 26(X)



CHOK4U7-500MA-1



FOR INTERNAL SWR
AR8151: LR36(O), LR38(X)
AR8161: LR38(O), LR36(X)

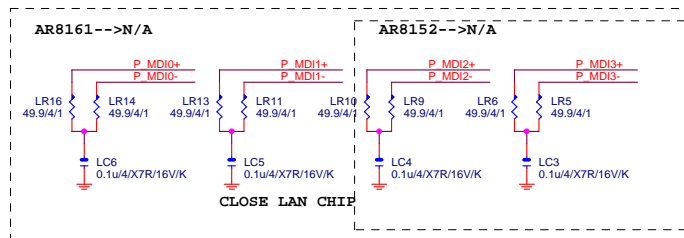
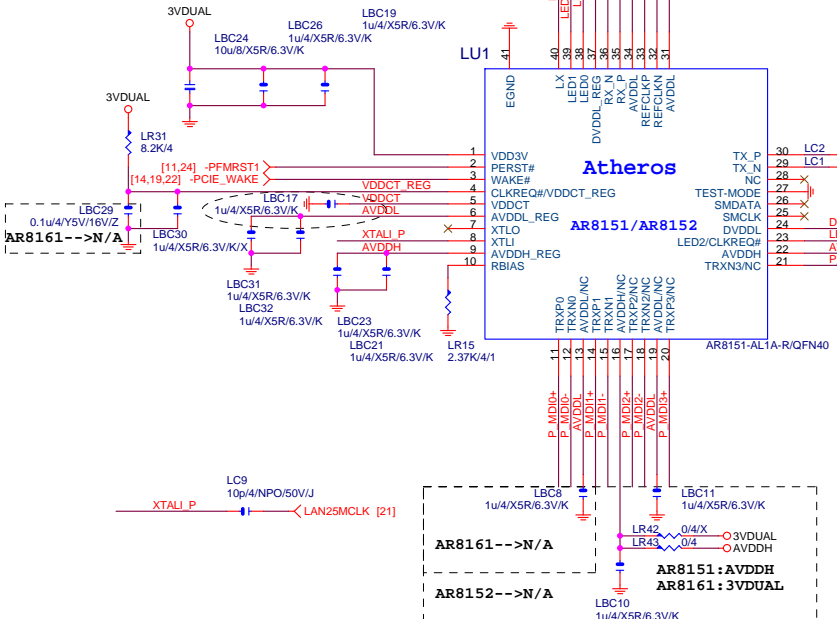
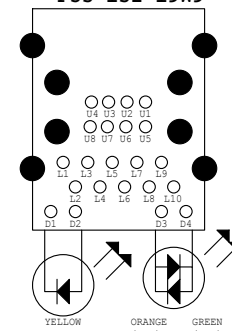


LED0 (ACT) LED1 (LINK) LED2 (LINK1G)

CLKREQ# PULL-UP FOR EFUSE:

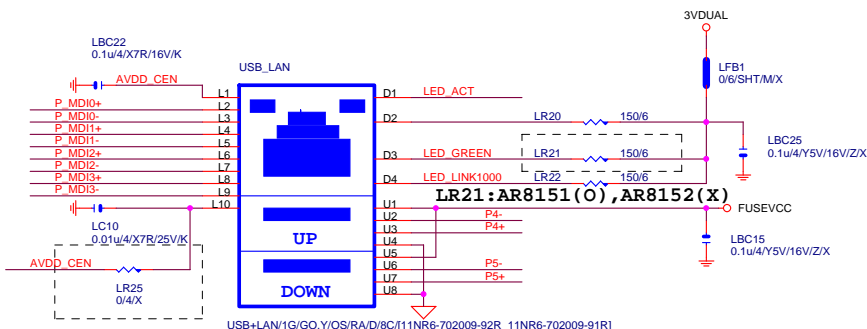
1. AR8151: LR31(O), LR32(X)
2. AR8152: LR31(X), LR32(O)

P35-152-19W9

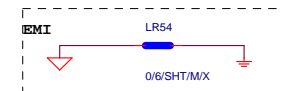
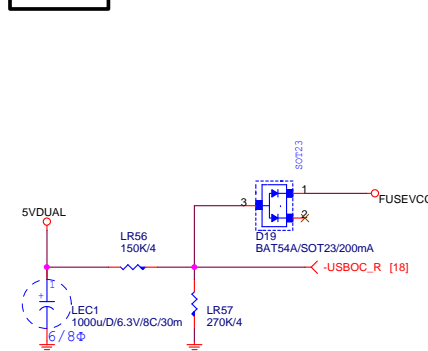


USB_LAN CONNECTOR

1G : USB+LAN/1G/GO, Y/OS/RA/D/1
100M: USB+LAN/100/GO, Y/OS/RA/D/1



USB_LAN



LR25: AR8151(X), AR8152(O)
因100M LAN
CONNECTOR差異

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ARTHEROS AR8151/AR8161			
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